

Q5  
3. (Amended) A method [Method] according to claim 1, further comprising the sequential steps of: [characterized in that]

5                   recognizing said [the] outage of one of said [the] peripheral line assemblies [(for example, BG<sub>1</sub>) is additionally recognized] by an interface [interfaces [sic] (AMX)] belonging to said [the] switching network; and [,]  
                  sending, by said interface belonging to said switching network, [where upon this sends] a corresponding message [(M<sub>LPS</sub>)] to said [the] higher-ranking mechanism [means (MPSA)].

IN THE ABSTRACT:

- 10                  On page 7:  
                     cancel lines 2-3;  
                     in line 4, replace "means" with -mechanism-;  
                     replace line 5 with assemblies in 1:N redundancy, resulting in a system dynamics loss. The-;
- 15                  in line 7, replace "means" with -mechanism-;  
                     in line 9, before the last "the", insert -relieving-;  
                     in line 10, replace "means being thus relieved" with -mechanism-; and  
                     cancel line 11.

REMARKS

- 20                  The present Amendment revises the specification and claims to conform to United States patent practice, before examination of the present PCT application in the United States National Examination Phase. All of the changes are editorial and applicant believes no new matter is added thereby. The amendment of claims 1-3 is not intended to be a surrender of any of the subject matter of those claims.
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